Evaluation of a Heterogeneous Multicore Architecture by Design and Test of an OFDM Receiver
Research output: Scientific - peer-review › Article

Proc. 2017 IEEE Nordic Circuits and Systems Conference (NORCAS)
Research output: Scientific - peer-review › Anthology

MergeTree: A Fast Hardware HLBVH Constructor for Animated Ray Tracing
Research output: Scientific - peer-review › Article

Fast Hardware Construction and Refitting of Quantized Bounding Volume Hierarchies
Research output: Scientific - peer-review › Article

Integration Issues of a run-Time Configurable Memory Management Unit to a RISC Processor on FPGA
Research output: Scientific - peer-review › Article

Computing Platforms for Software-Defined Radio
Research output: Scientific - peer-review › Anthology

Data Flow Algorithms for Processors with Vector Extensions: Handling Actors With Internal State
Research output: Scientific - peer-review › Article

Design and Implementation of IEEE 802.11a/g Receiver Blocks on a Coarse-Grained Reconfigurable Array
Research output: Scientific - peer-review › Chapter

Design Transformation from a Single-Core to a Multi-Core Architecture Targeting Massively Parallel Signal Processing Algorithms
Research output: Scientific - peer-review › Chapter

Efficient fast-convolution based implementation of 5G waveform processing using circular convolution decomposition
Research output: Scientific - peer-review › Conference contribution

Filtered multitone multicarrier modulation with partially overlapping sub-channels
Research output: Scientific - peer-review › Conference contribution

Introduction and Book Structure
Multi-Technology Positioning
Research output: Scientific - peer-review › Anthology

Ninesilica: A Homogeneous MPSoC Approach for SDR Platforms
Research output: Scientific - peer-review › Chapter

Proceedings of 2017 27th International Conference on Field Programmable Logic and Applications (FPL)
Research output: Scientific - peer-review › Anthology

Synchronization in NC-OFDM-Based Cognitive Radio Platforms
Research output: Scientific - peer-review › Chapter

The Evolution of Software-Defined Radio: An Introduction
Research output: Scientific - peer-review › Chapter

The Future of Software-Defined Radio: Recommendations
Research output: Scientific - peer-review › Chapter

Xor-Masking: A Novel Statistical Method for Instruction Read Energy Reduction in Contemporary SRAM Technologies
Research output: Scientific - peer-review › Conference contribution

FPGA Implementation Issues of a Flexible Synchronizer Suitable for NC-OFDM-Based Cognitive Radios
Research output: Scientific - peer-review › Article

Using OpenCL to Rapidly Prototype FPGA Designs
Research output: Scientific - peer-review › Conference contribution

Better, Faster Systems with Heterogeneous Design
Research output: General public › Article

Accelerating Computation on an Android Phone with OpenCL Parallelism and Optimizing Workload Distribution between a Phone and a Cloud Service
Research output: Scientific - peer-review › Conference contribution
Towards automation security research and training environment
Research output: Scientific - peer-review › Conference contribution

Midair User Interfaces Employing Particle Screens
Research output: Scientific - peer-review › Article

A Hybrid Task Graph Scheduler for High Performance Image Processing Workflows
Research output: Scientific - peer-review › Conference contribution

An efficient GPU implementation of a multirate resampler for multi-carrier systems
Research output: Scientific - peer-review › Conference contribution

Design and Implementation of a Power-aware FFT Core for OFDM-based DSA-enabled Cognitive Radios
Research output: Scientific - peer-review › Article

Head-mounted Display with Mid-air Tactile Feedback
Research output: Scientific - peer-review › Conference contribution

Kvazaar HEVC encoder for Efficient Intra Coding
Research output: Scientific - peer-review › Conference contribution

Parallelization of Kvazaar HEVC Intra Encoder for Multi-core Processors
Research output: Scientific - peer-review › Conference contribution

Pedestrian Localization in Moving Platforms Using Dead Reckoning, Particle Filtering and Map Matching
Research output: Scientific - peer-review › Conference contribution

Performance Evaluation of Kvazaar HEVC Intra Encoder on Xeon Phi Many-core Processor
Research output: Scientific - peer-review › Conference contribution

Power Optimizations for Transport Triggered SIMD Processors
Programmable Data Parallel Accelerator for Mobile Computer Vision

Resolving parameter reference management in IP-XACT using Kactus2

WarmPie: A bare-bones implementation of message passing interface for embedded many-cores

2013 International Symposium on System-on-Chip Proceedings

Accomodating the fast-paced evolution of VLSI in engineering curricula

Advanced Acquisition and Tracking Algorithms

Approximate computing for complexity reduction in timing synchronization

Area estimation of time-domain GNSS receiver architectures

Baseband Hardware Implementations for Galileo Receiver

Conclusions and Outlook

Constraint-driven frequency scaling in a Coarse Grain Reconfigurable Array
Cyclostationary features of downsampled 802.11g OFDM signal for cognitive positioning systems
Research output: Scientific - peer-review › Conference contribution

Design of a Flexible, Energy Efficient (Auto)Correlator Block for Timing Synchronization
Research output: Scientific - peer-review › Conference contribution

Design of an accelerator-rich architecture by integrating multiple heterogeneous coarse grain reconfigurable arrays over a network-on-chip
Research output: Scientific - peer-review › Conference contribution

Enabling Real-Time Resource Oriented Architectures with REST Observers
Research output: Scientific - peer-review › Chapter

Faster than real-time GNSS receiver testing
Research output: Scientific - peer-review › Conference contribution

FPGA Implementation of a Flexible Synchronizer for Cognitive Radio Applications
Research output: Scientific - peer-review › Conference contribution

GALILEO Positioning Technology
Research output: Scientific - peer-review › Anthology

Galileo Signals
Research output: Scientific - peer-review › Chapter

Hand-grip Impact on range-based cooperative positioning
Research output: Scientific - peer-review › Conference contribution

High-level parameterizable area estimation modeling for ASIC designs
Research output: Scientific - peer-review › Article
Implementation of Multicore Communications API
Research output: Scientific - peer-review › Conference contribution

Improving UX work in Scrum Development: A Three-Year Follow-Up Study in a Company
Research output: Scientific - peer-review › Conference contribution

MPSoC based on Transport Triggered Architecture for Baseband Processing of an LTE Receiver
Research output: Scientific - peer-review › Article

MULTI-POS - Multi-technology Positioning Professionals Training Network
Research output: Scientific - peer-review › Conference contribution

PVT Computation Issues in Mixed Galileo/GPS Reception
Research output: Scientific - peer-review › Chapter

Software Simulators and Multi-Frequency Test Scenarios for GALILEO
Research output: Scientific - peer-review › Chapter

Special Issue on Embedded Computer Systems: Architectures, Modeling and Simulation
McAllister, J., Guevorkian, D., Jeschke, H. & Sima, M. 2014 In : International Journal of Parallel Programming. 43, 1
Research output: Scientific - peer-review › Special issue

Tactical Applications of Heterogeneous Ad Hoc Networks - Cognitive Radios, Wireless Sensor Networks and COTS in Networked Mobile Operations
Research output: Scientific - peer-review › Conference contribution

Training communication skills in project-oriented microelectronics courses
Research output: Scientific - peer-review › Conference contribution

Transport triggered architecture to perform carrier synchronization for LTE
Research output: Scientific - peer-review › Article