Integration Issues of a Run-Time Configurable Memory Management Unit to a RISC Processor on FPGA
Research output: Scientific - peer-review › Article

FPGA Implementation Issues of a Flexible Synchronizer Suitable for NC-OFDM-Based Cognitive Radios
Research output: Scientific - peer-review › Article

Using OpenCL to Rapidly Prototype FPGA Designs
Research output: Scientific - peer-review › Conference contribution

Better, Faster Systems with Heterogeneous Design
Research output: General public › Article

Accelerating Computation on an Android Phone with OpenCL Parallelism and Optimizing Workload Distribution between a Phone and a Cloud Service
Research output: Scientific - peer-review › Conference contribution

MULTI-POS: Marie Curie Network in Multi-technology Positioning
Research output: Scientific - peer-review › Conference contribution

Research output: Scientific - peer-review › Article

IEEE 802.11ac MIMO Transceiver Baseband Processing on a VLIW Processor
Research output: Scientific - peer-review › Article

Impact of Operand Sharing to the Processor Energy Efficiency
Research output: Scientific - peer-review › Conference contribution

OpenCL Programmable Exposed Datapath High Performance Low-Power Image Signal Processor
Research output: Scientific - peer-review › Conference contribution

Optimization of parallel processing intensive digital front-end for IEEE 802.11ac receiver
Research output: Scientific - peer-review › Conference contribution
Remotely Powered Piezoresistive Pressure Sensor: Toward Wireless Monitoring of Intracranial Pressure
Research output: Scientific - peer-review › Article

HSA-Enabled DSPs and Accelerators
Research output: Scientific - peer-review › Conference contribution

Model-based design and implementation of an adaptive digital predistortion filter
Research output: Scientific - peer-review › Conference contribution

E-learning of ethics, awareness, hacking and research by information security majors
Research output: Scientific - peer-review › Conference contribution

MEMS IMU Carouselling for Ground Vehicles
Collin, J. 16 Jun 2015 In : IEEE Transactions on Vehicular Technology. 64, 6, p. 2242-2251
Research output: Scientific - peer-review › Article

Constant-rate clock recovery and jitter measurement on deep memory waveforms using dataflow
Research output: Scientific - peer-review › Conference contribution

Towards automation security research and training environment
Research output: Scientific - peer-review › Conference contribution

Midair User Interfaces Employing Particle Screens
Research output: Scientific - peer-review › Article

A Hybrid Task Graph Scheduler for High Performance Image Processing Workflows
Research output: Scientific - peer-review › Conference contribution

An efficient GPU implementation of a multirate resampler for multi-carrier systems
Research output: Scientific - peer-review › Conference contribution

Design and Implementation of a Power-aware FFT Core for OFDM-based DSA-enabled Cognitive Radios
Research output: Scientific - peer-review › Article

Head-mounted Display with Mid-air Tactile Feedback
Kvazaar HEVC encoder for Efficient Intra Coding

Parallelization of Kvazaar HEVC Intra Encoder for Multi-core Processors

Pedestrian Localization in Moving Platforms Using Dead Reckoning, Particle Filtering and Map Matching

Performance Evaluation of Kvazaar HEVC Intra Encoder on Xeon Phi Many-core Processor

Power Optimizations for Transport Triggered SIMD Processors

Programmable Data Parallel Accelerator for Mobile Computer Vision

Resolving parameter reference management in IP-XACT using Kactus2

WarmPie: A bare-bones implementation of message passing interface for embedded many-cores

Advanced Acquisition and Tracking Algorithms
Approximate computing for complexity reduction in timing synchronization

Area estimation of time-domain GNSS receiver architectures

Baseband Hardware Implementations for Galileo Receiver

Conclusions and Outlook

Constraint-driven frequency scaling in a Coarse Grain Reconfigurable Array

Cyclostationary features of downsampled 802.11g OFDM signal for cognitive positioning systems

Design of a Flexible, Energy Efficient (Auto)Correlator Block for Timing Synchronization

Design of an accelerator-rich architecture by integrating multiple heterogeneous coarse grain reconfigurable arrays over a network-on-chip

Enabling Real-Time Resource Oriented Architectures with REST Observers

Faster than real-time GNSS receiver testing
Special Issue on Embedded Computer Systems: Architectures, Modeling and Simulation
McAllister, J., Guevorkian, D., Jeschke, H. & Sima, M. 2014 In : International Journal of Parallel Programming. 43, 1
Research output: Scientific - peer-review › Special issue

Tactical Applications of Heterogeneous Ad Hoc Networks - Cognitive Radios, Wireless Sensor Networks and COTS in Networked Mobile Operations
Research output: Scientific - peer-review › Conference contribution

Training communication skills in project-oriented microelectronics courses
Research output: Scientific - peer-review › Conference contribution

Transport triggered architecture to perform carrier synchronization for LTE
Research output: Scientific - peer-review › Article