Evaluation of a Heterogeneous Multicore Architecture by Design and Test of an OFDM Receiver
Research output: Scientific - peer-review › Article

Proc. 2017 IEEE Nordic Circuits and Systems Conference (NORCAS)
Research output: Scientific - peer-review › Anthology

MergeTree: A Fast Hardware HLBVH Constructor for Animated Ray Tracing
Research output: Scientific - peer-review › Article

Fast Hardware Construction and Refitting of Quantized Bounding Volume Hierarchies
Research output: Scientific - peer-review › Article

Computing Platforms for Software-Defined Radio
Research output: Scientific - peer-review › Anthology

Data Flow Algorithms for Processors with Vector Extensions: Handling Actors With Internal State
Research output: Scientific - peer-review › Article

Design and Implementation of IEEE 802.11a/g Receiver Blocks on a Coarse-Grained Reconfigurable Array
Research output: Scientific - peer-review › Chapter

Efficient fast-convolution based implementation of 5G waveform processing using circular convolution decomposition
Research output: Scientific - peer-review › Conference contribution

Filtered multitone multicarrier modulation with partially overlapping sub-channels
Research output: Scientific - peer-review › Conference contribution

Introduction and Book Structure
Research output: Scientific - peer-review › Chapter

Multi-Technology Positioning
MULTI-POS: Marie Curie Network in Multi-technology Positioning
Research output: Scientific - peer-review › Conference contribution

Research output: Scientific - peer-review › Article

IEEE 802.11ac MIMO Transceiver Baseband Processing on a VLIW Processor
Research output: Scientific - peer-review › Article

Impact of Operand sharing to the Processor Energy Efficiency
Research output: Scientific - peer-review › Conference contribution

OpenCL Programmable Exposed Datapath High Performance Low-Power Image Signal Processor
Research output: Scientific - peer-review › Conference contribution

Optimization of parallel processing intensive digital front-end for IEEE 802.11ac receiver
Research output: Scientific - peer-review › Conference contribution

Remotely Powered Piezoresistive Pressure Sensor: Toward Wireless Monitoring of Intracranial Pressure
Research output: Scientific - peer-review › Article

HSA-Enabled DSPs and Accelerators
Research output: Scientific - peer-review › Conference contribution

Model-based design and implementation of an adaptive digital predistortion filter
Research output: Scientific - peer-review › Conference contribution

E-learning of ethics, awareness, hacking and research by information security majors
Research output: Scientific - peer-review › Conference contribution

MEMS IMU Carouselling for Ground Vehicles
Collin, J. 16 Jun 2015 In : IEEE Transactions on Vehicular Technology. 64, 6, p. 2242-2251
Research output: Scientific - peer-review › Article

Constant-rate clock recovery and jitter measurement on deep memory waveforms using dataflow
Programmable Data Parallel Accelerator for Mobile Computer Vision

Resolving parameter reference management in IP-XACT using Kactus2

WarmPie: A bare-bones implementation of message passing interface for embedded many-cores

2013 International Symposium on System-on-Chip Proceedings

Accommodating the fast-paced evolution of VLSI in engineering curricula

Advanced Acquisition and Tracking Algorithms

Approximate computing for complexity reduction in timing synchronization

Area estimation of time-domain GNSS receiver architectures

Baseband Hardware Implementations for Galileo Receiver

Conclusions and Outlook

Constraint-driven frequency scaling in a Coarse Grain Reconfigurable Array
Cyclostationary features of downsampled 802.11g OFDM signal for cognitive positioning systems
Research output: Scientific › peer-review › Conference contribution

Design of a Flexible, Energy Efficient (Auto)Correlator Block for Timing Synchronization
Research output: Scientific › peer-review › Conference contribution

Design of an accelerator-rich architecture by integrating multiple heterogeneous coarse grain reconfigurable arrays over a network-on-chip
Research output: Scientific › peer-review › Conference contribution

Enabling Real-Time Resource Oriented Architectures with REST Observers
Research output: Scientific › peer-review › Chapter

Faster than real-time GNSS receiver testing
Research output: Scientific › peer-review › Conference contribution

FPGA Implementation of a Flexible Synchronizer for Cognitive Radio Applications
Research output: Scientific › peer-review › Conference contribution

GALILEO Positioning Technology
Research output: Scientific › peer-review › Anthology

Gallileo Signals
Research output: Scientific › peer-review › Chapter

Hand-grip impact on range-based cooperative positioning
Research output: Scientific › peer-review › Conference contribution

High-level parameterizable area estimation modeling for ASIC designs
Research output: Scientific › peer-review › Article
Improving UX work in Scrum Development: A Three-Year Follow-Up Study in a Company
Research output: Scientific - peer-review › Conference contribution

MPSoC based on Transport Triggered Architecture for Baseband Processing of an LTE Receiver
Research output: Scientific - peer-review › Article

MULTI-POS - Multi-technology Positioning Professionals Training Network
Research output: Scientific - peer-review › Conference contribution

PVT Computation Issues in Mixed Galileo/GPS Reception
Research output: Scientific - peer-review › Chapter

Software Simulators and Multi-Frequency Test Scenarios for GALILEO
Research output: Scientific - peer-review › Chapter

Special Issue on Embedded Computer Systems: Architectures, Modeling and Simulation
McAllister, J., Guevorkian, D., Jeschke, H. & Sima, M. 2014 In : International Journal of Parallel Programming. 43, 1
Research output: Scientific - peer-review › Special issue

Tactical Applications of Heterogeneous Ad Hoc Networks - Cognitive Radios, Wireless Sensor Networks and COTS in Networked Mobile Operations
Research output: Scientific - peer-review › Conference contribution

Training communication skills in project-oriented microelectronics courses
Research output: Scientific - peer-review › Conference contribution

Transport triggered architecture to perform carrier synchronization for LTE
Research output: Scientific - peer-review › Article