Research output: Scientific - peer-review » Conference contribution

IEEE 802.11ac MIMO Transceiver Baseband Processing on a VLIW Processor
Research output: Scientific - peer-review » Article

Impact of Operand sharing to the Processor Energy Efficiency
Research output: Scientific - peer-review » Conference contribution

OpenCL Programmable Exposed Datapath High Performance Low-Power Image Signal Processor
Research output: Scientific - peer-review » Conference contribution

Optimization of parallel processing intensive digital front-end for IEEE 802.11ac receiver
Research output: Scientific - peer-review » Conference contribution

Remotely Powered Piezoresistive Pressure Sensor: Toward Wireless Monitoring of Intracranial Pressure
Research output: Scientific - peer-review » Article

HSA-Enabled DSPs and Accelerators
Research output: Scientific - peer-review » Conference contribution

Model-based design and implementation of an adaptive digital predistortion filter
Research output: Scientific - peer-review » Conference contribution

E-learning of ethics, awareness, hacking and research by information security majors
Research output: Scientific - peer-review » Conference contribution

MEMS IMU Carouselling for Ground Vehicles
Collin, J. 16 Jun 2015 In : IEEE Transactions on Vehicular Technology. 64, 6, p. 2242-2251
Research output: Scientific - peer-review » Article

Constant-rate clock recovery and jitter measurement on deep memory waveforms using dataflow
Research output: Scientific - peer-review » Conference contribution
Towards automation security research and training environment
Research output: Scientific - peer-review › Conference contribution

Midair User Interfaces Employing Particle Screens
Research output: Scientific - peer-review › Article

A Hybrid Task Graph Scheduler for High Performance Image Processing Workflows
Research output: Scientific - peer-review › Conference contribution

An efficient GPU implementation of a multirate resampler for multi-carrier systems
Research output: Scientific - peer-review › Conference contribution

Design and Implementation of a Power-aware FFT Core for OFDM-based DSA-enabled Cognitive Radios
Research output: Scientific - peer-review › Article

Head-mounted Display with Mid-air Tactile Feedback
Research output: Scientific - peer-review › Conference contribution

Kvazaar HEVC encoder for Efficient Intra Coding
Research output: Scientific - peer-review › Conference contribution

Parallelization of Kvazaar HEVC Intra Encoder for Multi-core Processors
Research output: Scientific - peer-review › Conference contribution

Pedestrian Localization in Moving Platforms Using Dead Reckoning, Particle Filtering and Map Matching
Research output: Scientific - peer-review › Conference contribution

Performance Evaluation of Kvazaar HEVC Intra Encoder on Xeon Phi Many-core Processor
Research output: Scientific - peer-review › Conference contribution

Power Optimizations for Transport Triggered SIMD Processors
Research output: Scientific - peer-review › Conference contribution
Programmable Data Parallel Accelerator for Mobile Computer Vision
Research output: Scientific - peer-review › Conference contribution

Resolving parameter reference management in IP-XACT using Kactus2
Research output: Scientific - peer-review › Conference contribution

WarmPie: A bare-bones implementation of message passing interface for embedded many-cores
Research output: Scientific - peer-review › Conference contribution

2013 International Symposium on System-on-Chip Proceedings
Research output: Scientific - peer-review › Anthology

Accomodating the fast-paced evolution of VLSI in engineering curricula
Research output: Scientific - peer-review › Conference contribution

Advanced Acquisition and Tracking Algorithms
Research output: Scientific - peer-review › Chapter

Approximate computing for complexity reduction in timing synchronization
Research output: Scientific - peer-review › Article

Area estimation of time-domain GNSS receiver architectures
Research output: Scientific - peer-review › Conference contribution

Baseband Hardware Implementations for Galileo Receiver
Research output: Scientific - peer-review › Chapter

Conclusions and Outlook
Research output: Scientific - peer-review › Chapter

Constraint-driven frequency scaling in a Coarse Grain Reconfigurable Array
Research output: Scientific - peer-review › Conference contribution
Cyclostationary features of downsampled 802.11g OFDM signal for cognitive positioning systems
Research output: Scientific › peer-review › Conference contribution

Design of a Flexible, Energy Efficient (Auto)Correlator Block for Timing Synchronization
Research output: Scientific › peer-review › Conference contribution

Design of an accelerator-rich architecture by integrating multiple heterogeneous coarse grain reconfigurable arrays over a network-on-chip
Research output: Scientific › peer-review › Conference contribution

Enabling Real-Time Resource Oriented Architectures with REST Observers
Research output: Scientific › peer-review › Chapter

Faster than real-time GNSS receiver testing
Research output: Scientific › peer-review › Conference contribution

FPGA Implementation of a Flexible Synchronizer for Cognitive Radio Applications
Research output: Scientific › peer-review › Conference contribution

GALILEO Positioning Technology
Research output: Scientific › peer-review › Anthology

Galileo Signals
Research output: Scientific › peer-review › Chapter

Hand-grip impact on range-based cooperative positioning
Research output: Scientific › peer-review › Conference contribution

High-level parameterizable area estimation modeling for ASIC designs
Research output: Scientific › peer-review › Article